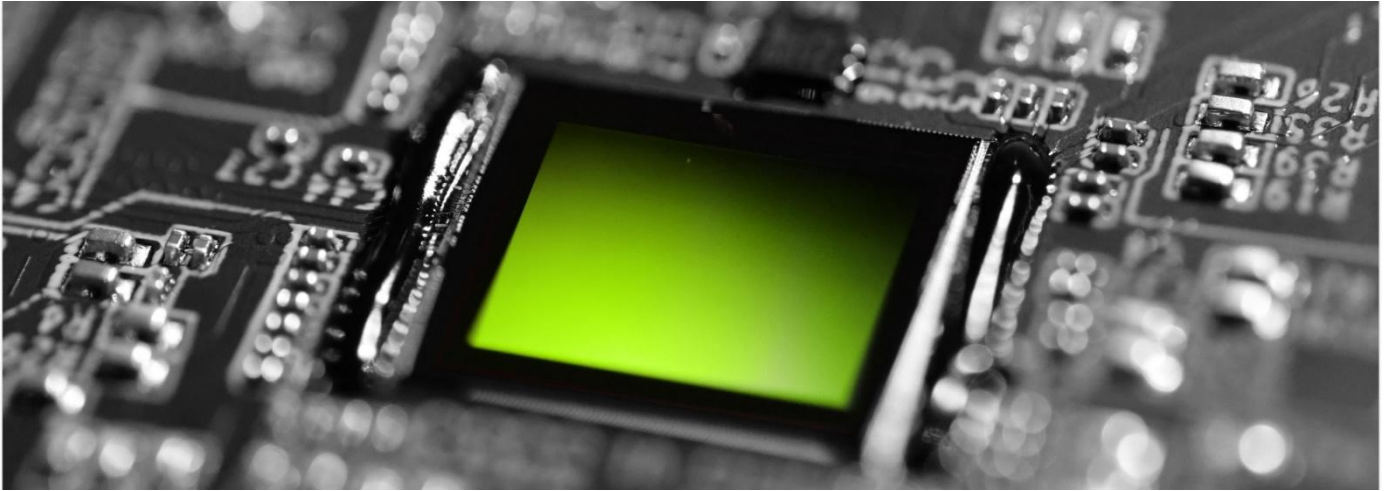
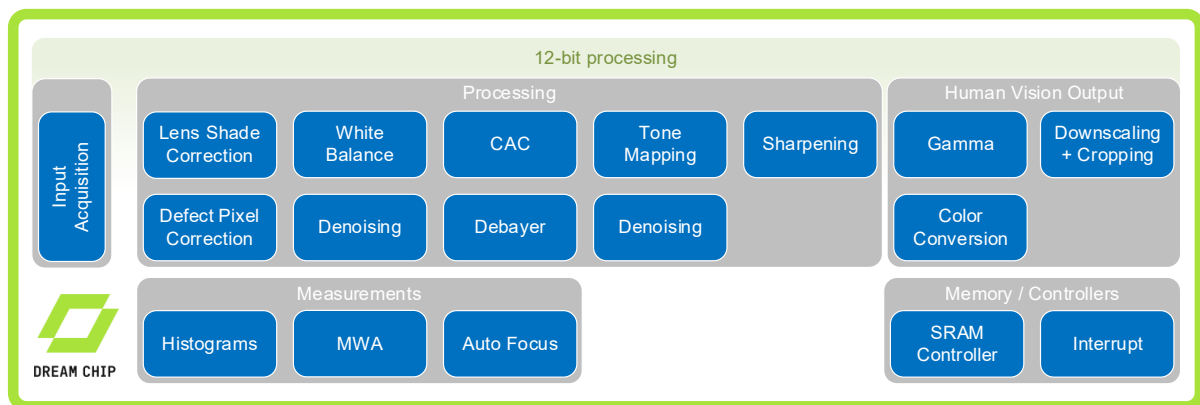




H Series: *ISP-H114*

Dream Chip's Real-time Pixel Processor for Vision applications is a scalable and configurable Standard Dynamic Range (SDR) capable image signal processor (ISP). The processing pipeline consists of all necessary modules allowing to output the highest possible quality.

The H Series provides configurable input and output data widths. It accepts YCbCr or RAW input data and supports input image frames with 8, 10 and 12-bit amplitude resolutions.



The H1 Series are ISPs which are optimized for area while getting the best image quality. Options to extend the pipeline with a Machine Vision output or configuring the line size to further optimize area are possible.

Extensions for functional safety can be included in accordance with ISO26262 to support automotive applications.

Specification:

Sensor input	Bayer / clear pixel / mono RGGB / RCCB / RCCC Configurable 12-bit input – up to 64 Mpix resolution
Throughput	1.3 gigapixels / second e.g. 16 MP @ 60 fps, 640x480 @ 240 fps
ISP output	Streaming output for Human Vision (Machine Vision – optional)

ISP-Advantages:

- Modular approach
- Scalable resolution up to 64 MP
- Customizations
- Low latency
- Area-optimized
- FPGA validation platforms available
- Tests with different sensors
- Calibration tools
- Bit-true simulation tools
- Software example code

Applications:

- Consumer
- Wearables
- Industrial
- Image capture
- Digital video recording

[Dream Chip's ISP](#) is designed with a modular approach, allowing seamless scalability to match a wide range of application requirements. This flexibility enables customers to customize functionality, balancing performance, power efficiency, and silicon area optimization. With dynamic resolution from 12 to 28 bits, resolutions up to 64 MP, 120 fps, it delivers **high-speed, high-quality image processing**. Its adaptable architecture allows new features to be integrated or functions to be optimized, ensuring optimal efficiency, reduced power consumption, and a tailored imaging pipeline.

Looking for a **flexible, high-performance ISP solution** tailored to your needs? Let's discuss how [Dream Chip](#) can support your project with **scalable architecture, cutting-edge performance, and expert customization**. **Get in touch with our ISP experts today.**

